

FIG. 1 is a block diagram of a system 100. The system 100 includes an Embedded Core 110, an OCM 120, and two G-Logic & Intercon. blocks 115-A and 115-B. The Embedded Core 110 has an OUT port and an IN port. The OCM 120 has an IN port and an OUT port. The G-Logic & Intercon. block 115-A has a GL&I Output 122. The G-Logic & Intercon. block 115-B has a GL&I Output 132. The system 100 also includes two Clock 109 inputs. The system 100 is divided into an Output Path 114 and an Input Path 113. The Output Path 114 includes the Embedded Core 110, the G-Logic & Intercon. block 115-A, and the OCM 120. The Input Path 113 includes the OCM 120, the G-Logic & Intercon. block 115-B, and the Embedded Core 110. The system 100 is configured to process data from the Embedded Core 110 through the Output Path 114 and the Input Path 113.

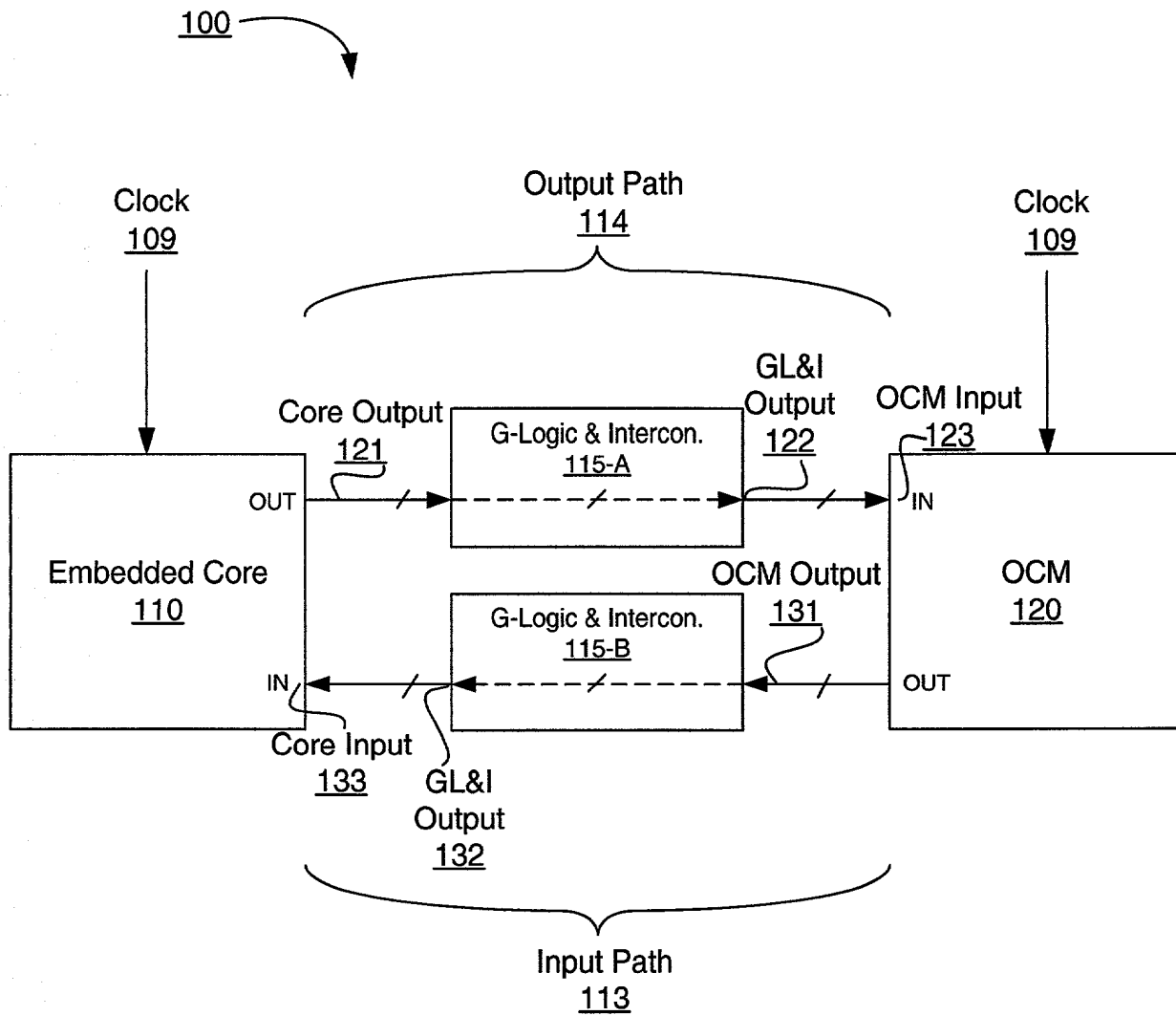


FIG. 1

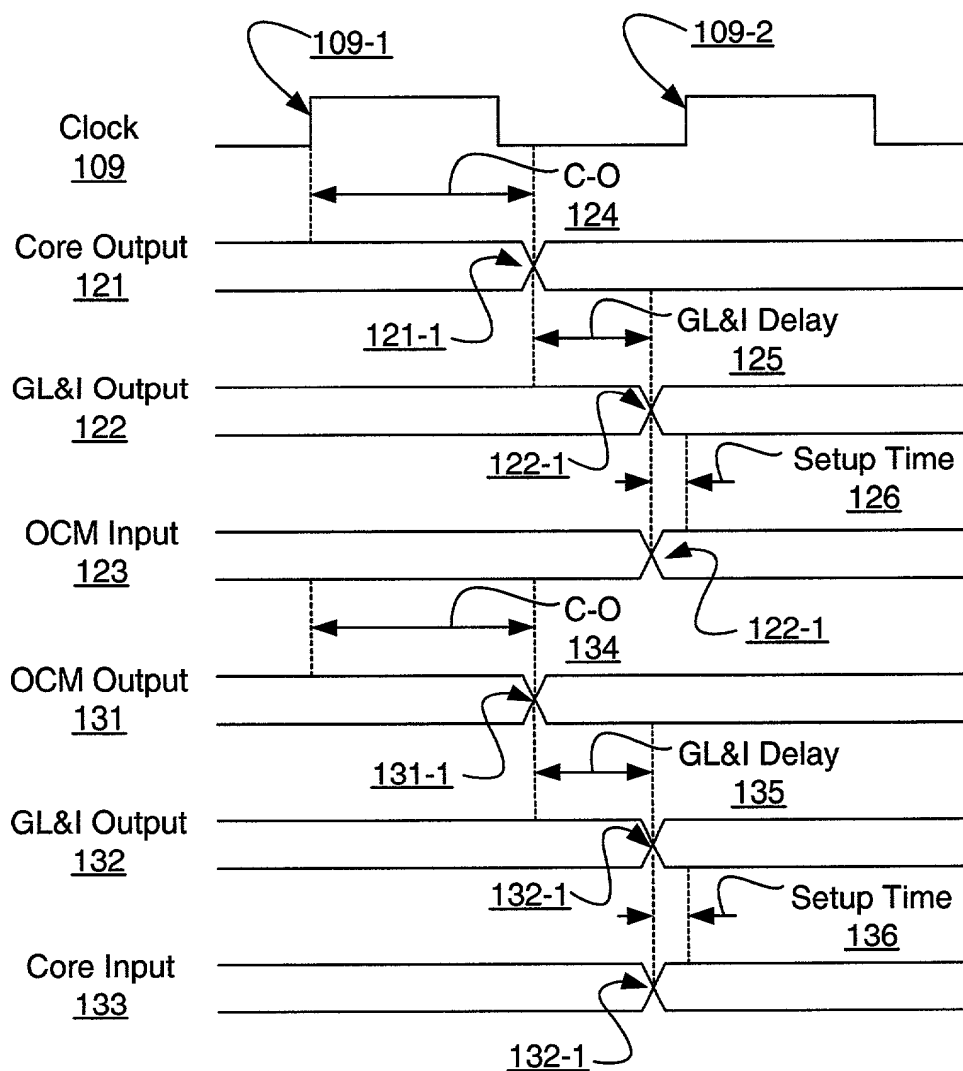


FIG. 2

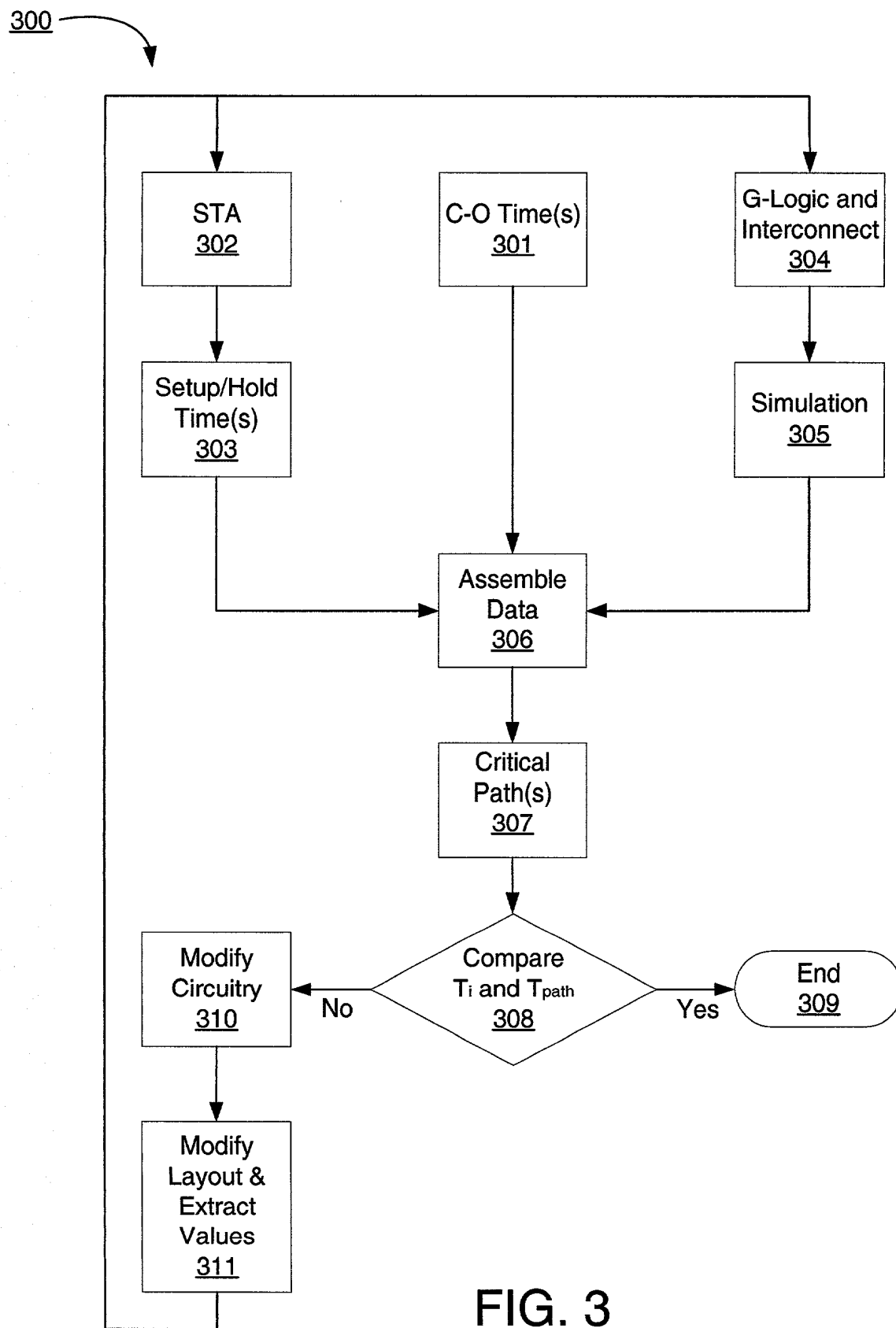


FIG. 3

